



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)	:	James Hakewill, et al.
Appl. No.	:	09/418,663
Filed	:	October 14, 1999
For	:	<b>METHOD AND APPARATUS FOR MANAGING THE CONFIGURATION AND FUNCTIONALITY OF A SEMICONDUCTOR DESIGN</b>
Group Art Unit	:	2123
Class-Subclass	:	703-014000
Examiner	:	Garcia Otero, Eduardo



27299

PATENT TRADEMARK OFFICE

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to:  
Mail Stop Issue Fee  
Attn: Official Draftsperson  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450, on

October 27, 2004  
(Date)

Robert F. Gazdzinski  
Reg. No. 39,990

## FORMAL DRAWING TRANSMITTAL LETTER

**Mail Stop Issue Fee  
Attn: Official Draftsperson  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**

Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) Forty-four (44) sheets of Formal Drawings containing forty-four (44) figures.
- (X) Return prepaid postcard.

These drawings are being submitted concurrently with payment of the issue fee.

GAZDZINSKI &amp; ASSOCIATES

Dated: 10/27/04

By: TBP

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